

PREPARED BY:    DATE	<b>SHARP</b>  LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION  <b>SPECIFICATION</b>	SPEC No. LC93714
APPROVED BY:    DATE		FILE No.
		ISSUE JUN. 13. 1993
		PAGE    24 Pages
		REPRESENTATIVE DIVISION
		■ New B/W LCD Project team

DEVICE SPECIFICATION for  
 Passive Matrix LCD Unit  
 (640×480 dots)

Model No.  
**LM64K101**

CUSTOMER'S APPROVAL

DATE \_\_\_\_\_

BY \_\_\_\_\_

PRESENTED BY Y. Inoue  
 Y. Inoue  
 Chief  
 New B/W LCD Project team  
 LCD GROUP  
 SHARP CORPORATION

## 3. Mechanical Specifications

Table 1

Parameter	Specifications	Unit
Outline dimensions	200.5 (W) × 141 (H) × 7MAX (D) #1#2	mm
Effective viewing Area	151 (W) × 113.4 (H)	mm
Display format	640 (W) × 480 (H) full dot	—
Dot size	0.21 × 0.21	mm
Dot spacing	0.02	mm
Dot color	Black#3#4	—
Background color	White#3#4	—
Weight	Approx. 260	g

#1 Excluded the mounting tabs.

#2 Excluded the allowance of deformation.

#3 Due to the characteristics of the LC material, the colors vary with environmental temperature.

#4 Positive-type display

Displayed data 'H': Dots ON : Black

Displayed data 'L': Dots OFF: White

## 4. Absolute Maximum Ratings

4-1 Electrical absolute maximum ratings

Table 2

Parameter	Symbol	MIN.	MAX.	Unit	Remark
Supply voltage (Logic)	$V_{DD}-V_{SS}$	0	6.0	V	Ta=25 °C
Supply voltage (LCD drive)	$V_{DD}-V_{EZ}$	0	28.0	V	Ta=25 °C
Input voltage	$V_{IN}$	0	$V_{DD}$	V	Ta=25 °C

## 5-3 Interface signals

Table 6

## ○LCD

Pin No	Symbol	Description	Level
1	S	Scan start-up signal	'H'
2	CP1	Input data latch signal	H→L
3	CP2	Data input clock signal	H→L
4	DISP	Display control signal	Display on .. 'H' off.. 'L'
5	VDD	Power supply for logic and LCD (+)	
6	VSS	Ground potential	
7	VEE	Power supply for LCD (-)	
8	DU0	Display data signal (Upper half)	H (ON), L (OFF)
9	DU1		
10	DU2		
11	DU3	Display data signal (Lower half)	H (ON), L (OFF)
12	DL0		
13	DL1		
14	DL2		
15	DL3		

## ○CCFT

Pin No	Symbol	Description	Level
1	GND	Ground line (from Inverter)	-
2	NC	-	-
3	NC	-	-
4	HV	High voltage line (from Inverter)	-

Note) Pin No. and its location are shown in Fig. 10.

## ○LCD

Used connector: 53261-1510 (MOLEX)

Mating connector: 51021-1500 (MOLEX)

## ○CCFT

Used connector: M63M83-04 (MITSUMI)

Mating connector: M60-04-30-114P (MITSUMI)

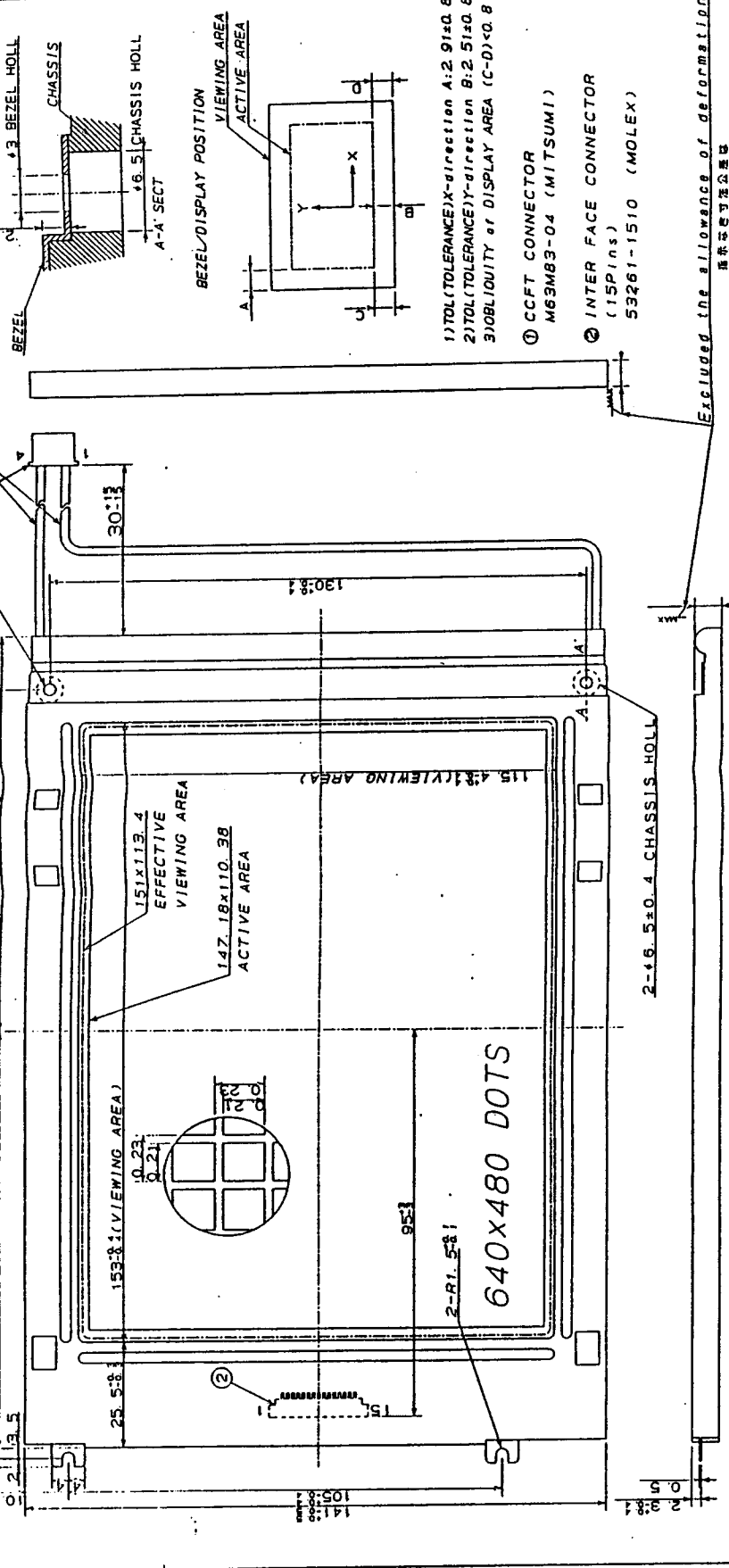
M60-04-30-134P (MITSUMI)

M61M73-04 (MITSUMI)

SHARP

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UL 3239  
106.5  
189.5±0.4  
104.5  
200.5±0.1  
102.8±0.1  
98.5  
2-46.5±0.4  
2-43.8±0.1  
30.1±0.1  
BEZEL  
CHASSIS  
A-A SECT  
±6.5 CHASSIS HOLL  
±3 BEZEL HOLL



Excluded the allowance of deformation.  
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UNSPECIFIED TOL TO BE

- 1) TOL (TOLERANCE) X-direction A: 2.91±0.8
- 2) TOL (TOLERANCE) Y-direction B: 2.51±0.8
- 3) OBLIQUITY of DISPLAY AREA (C-D) < 0.8

- ① CCFT CONNECTOR M63MB3-04 (MITSUMI)
- ② INTER FACE CONNECTOR (15PINS) 53261-1510 (MOLEX)

LM64K101		LCD UNIT OUTLINE DIMENSIONS	
DATE	DESIGNER	NAME	SYMBOL
MATERIAL	PROCESS	SCALE	PARTS CODE
DESIGN TRACK	CHECK APPROVAL	SHARP CORPORATION	DATE
DESIGNER	CHECKER	シヤープ株式会社 液晶(液晶)	1993-MAY-28
DESIGNER	CHECKER	液晶部 液晶班	DRAWING No.
DESIGNER	CHECKER	液晶部 液晶班	NET WT (G)

INTER FACE PIN LAYOUT

PIN#	1	2	3	4	5	6	7	8
SYMBOL	CP1	CP2	CP3	VDD	VSS	VEE	DVO	
PIN#	9	10	11	12	13	14	15	
SYMBOL	DU1	DU2	DU3	DU4	DU5	DU6	DU7	DU8

Fig. 10